

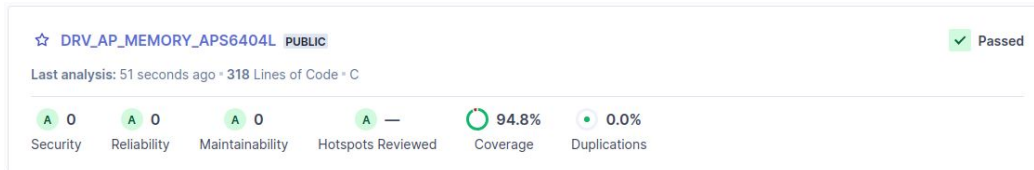
AP Memory APS6404L :: Pseudo-Static RAM (PSRAM)

The [AP Memory APS6404L](#) is a low-power 64Mb pseudo-static RAM (PSRAM). Its small dimensions, low pin count and high-speed Quad-SPI interface provide fast data access and efficient memory expansion for embedded systems. It supports data rates up to 133MHz and provides additional volatile memory for buffering, data processing and temporary data storage without requiring a large number of microcontroller pins.

Typical applications for this PSRAM are embedded and memory-intensive systems, including data loggers, IoT devices, edge AI devices, industrial equipment and other applications requiring additional high-speed volatile memory for data buffering, processing and temporary storage.

The software is architected with long-term maintainability, portability across MCU platforms, and adherence to high software quality standards as core design principles.

- ✓ Layered architecture with clear HAL abstraction
- ✓ Conforms to ISO C99 standard
- ✓ Portable across multiple MCU platforms
- ✓ Supports both RTOS and bare-metal environments
- ✓ CMake build system for scalable integration
- ✓ Seamless integration with GCC toolchain
- ✓ Statically analyzed for MISRA, CERT, and CWE compliance



Initialization Interface

```
aps6404l_result_t aps6404l_init( aps6404l_handle_t*, aps6404l_attr_t )
```

Data Retrieval Interface

```
aps6404l_result_t aps6404l_get_device_info( aps6404l_handle_t*, ... )
```

```
aps6404l_result_t aps6404l_read( aps6404l_handle_t*, ... )
```

```
aps6404l_result_t aps6404l_write( aps6404l_handle_t*, ... )
```

Communication Interface:

SPI