NXP LPC845

The LPC845 enhances development with low-power Cortex-M0+ MCU featuring flexible switch-matrix I/Os, multi-channel 12-bit ADC, SCTimer/PWM, capacitive touch, and multiple serial interfaces. Ideal for cost-sensitive designs, sensors, and compact industrial products, it enables reliable, fast time-to-market embedded solutions. Support package available: LQFP48

The BSP development is made with a NXP LPC845-BRK development board. The BSP features a CMake and GCC build system and built based on **Hardware Abstraction Layer (HAL)**. It requires an application configuration file, which allows the user to specify the CPU clock frequency, enable or disable RTOS, and further define project-level I/O and settings.

This microcontroller offers a range of key features, including support for DAC (Digital to Analog Converter) and plenty of communication channels for I2C, UART, and SPI.

Some peripherals provide a resource lock interface for thread-safe operation, including I2C, PWM, SPI, and UART.

Version: 0.7.3

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Component Diagram
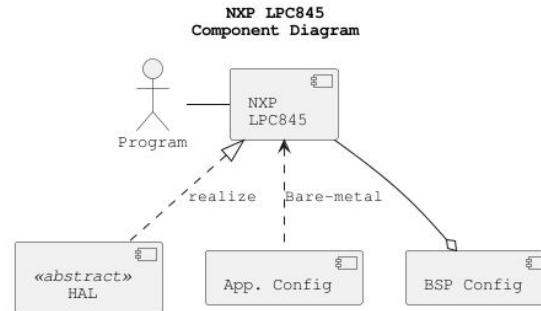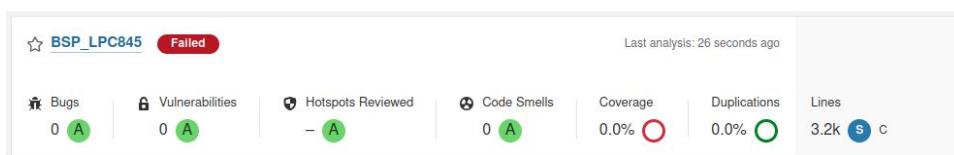
Available Peripherals
- ❏ ADC x12
- ❏ DAC x2
- ❏ GPIO x42
- ❏ I2C x4
- ❏ PWM x7
- ❏ SPI x2
- ❏ TIMER x4
- ❏ UART x3
- ❏ WATCHDOG

☆ **BSP_LPC845** **Failed**   Last analysis: 26 seconds ago

🐞 Bugs	🔒 Vulnerabilities	🛡 Hotspots Reviewed	☺ Code Smells	Coverage	Duplications	Lines
0 Ⓐ	0 Ⓐ	— Ⓐ	0 Ⓐ	0.0% ◯	0.0% ◯	3.2k Ⓢ c