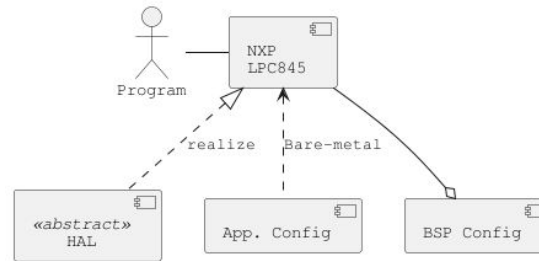The BSP development is made with a NXP LPC845-BRK development board. The BSP features a CMake and GCC build system. It requires an application configuration file, which allows the user to specify the CPU clock frequency, enable or disable RTOS, and further define project-level I/O and settings.

This microcontroller offers a range of key features, including support for DAC (Digital to Analog Converter) and plenty of communication channels such as I2C, UART, and SPI.

Some peripherals provide a resource lock interface for thread-safe operation, including I2C, PWM, SPI, and UART.

Version: v0.4.1

NXP LPC845
Component Diagram

Available Peripherals
- ❏ ADC x12
- ❏ DAC x2
- ❏ GPIO x42
- ❏ I2C x4
- ❏ PWM x7
- ❏ SPI x2
- ❏ TIMER x4
- ❏ UART x3
- ❏ WATCHDOG

☆ **BSP_LPC845**   **Failed**   Last analysis: 22 days ago

🐛 Bugs	🔒 Vulnerabilities	🛡 Hotspots Reviewed	☢ Code Smells	Coverage	Duplications	Lines
0 Ⓐ	0 Ⓐ	– Ⓐ	0 Ⓐ	0.0% ⭕	0.0% ⭕	2.9k Ⓢ c