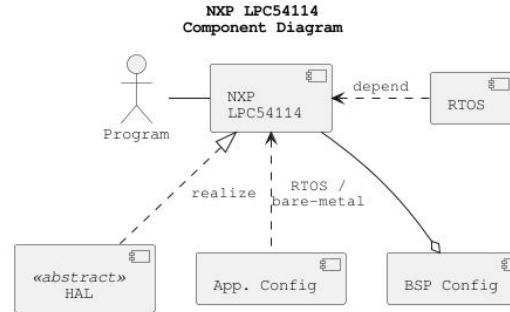


The BSP development is made with a NXP LPCXpresso54114 development board. The BSP features a CMake and GCC build system. It requires an application configuration file, which allows the user to specify the CPU clock frequency, enable or disable RTOS, and further define project-level I/O and settings.

This microcontroller offers a range of key features, including support for RTCC (Real Time Clock and Calendar) and numerous communication channels such as I2C, UART, and SPI.

Some peripherals provide a resource lock interface for thread-safe operation, including I2C, PWM, SPI, and UART.

Version: v0.6.0



Available Peripherals

- ADC x12
- GPIO x48
- I2C x8
- PWM x8
- RTCC x1
- SPI x8
- TIMER x5
- UART x8
- WATCHDOG

BSP_LPC54114 Failed Last analysis: 21 seconds ago

🐛 Bugs 0 A	🔒 Vulnerabilities 0 A	🔥 Hotspots Reviewed - A	👃 Code Smells 0 A	Coverage 0.0% C	Duplications 0.0% C	Lines 3.3k S C
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